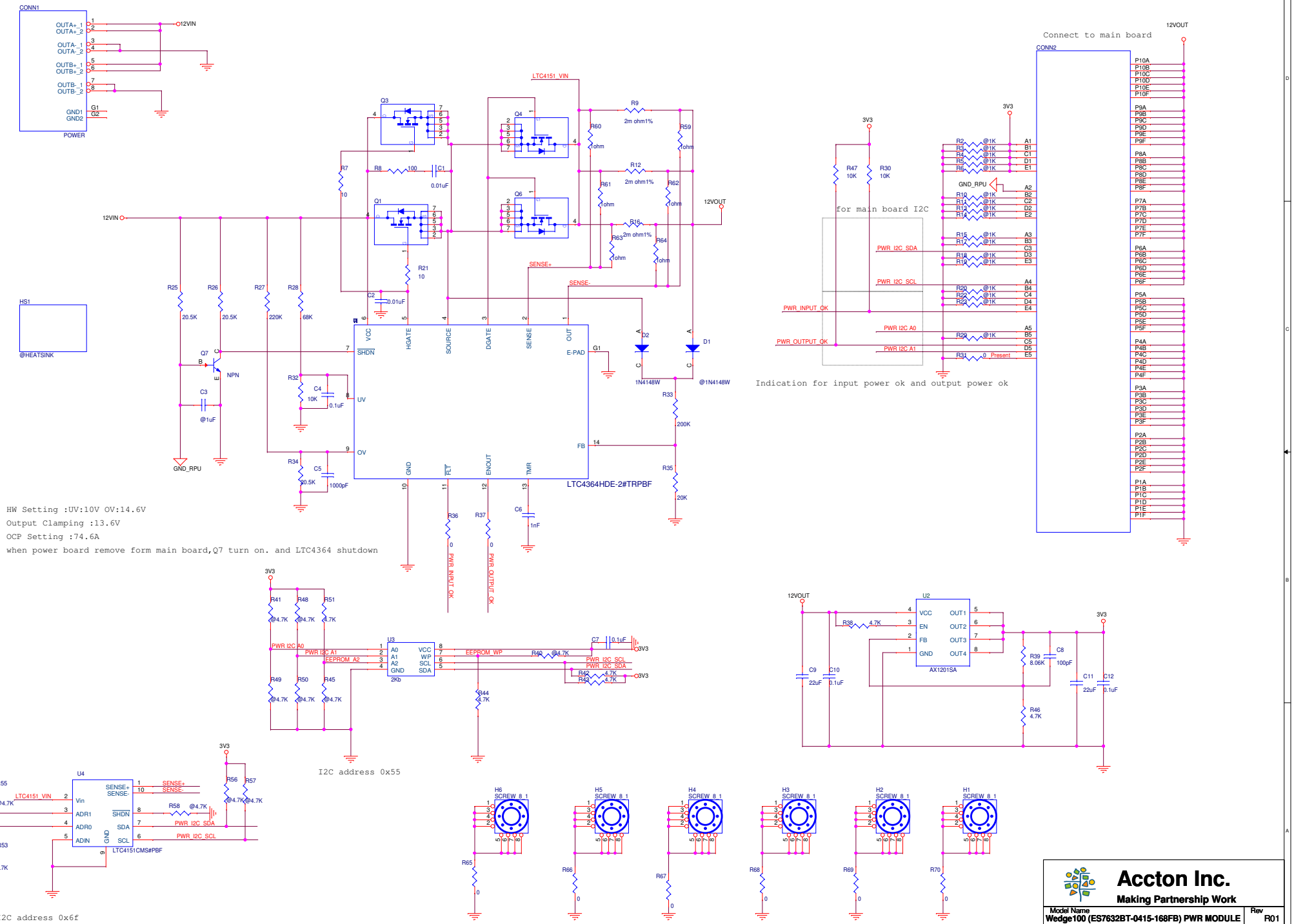
## Revision History

| Revision | Date | Change Description |
|---|---|---|
| Rev:R0A | 2015/05/25 | 1.Initial |
| | 2015/06/03 | 1. Remove Q2, Q5,R1, ADD SCREW H1~H6 ,ADD 2KV Capacitor C13~C18 |
| | 2015/06/19 | 2. Modify conn2 I2C SDA, SCL |
| Rev:R0B | 2015/08/19 | 1.Add LTC 4151 For Current Monitor |
| | 2015/08/24 | 1.Add I2C address U3:0X55, U4:0X37 |
| | 2015/08/27 | 1.Add Resistor to get good current sense result (R59~R64) |
| | 2015/12/22 | 1.Correction U4 I2C address 0x37 to 0x6f |
| | | 2.Change C13~ C18 form 1000pf cap to 0 ohm Resistor ( EMI solution) |
| | | 3.Change R28 from 38.3k to 68k ( change UV setting from 6V to 10V) |
| | | 4.Change R27 from 240k to 220k (change OV setting from16V to 14.6V) |
| Rev:R01 | 2016/01/26 | 1.Change R35 from 21k to 20k (change clamp voltage from13.2V to 13.6V) |
| | | 2.Remove C13~C18 |
| | | 3.Add R65~R70 |
| | | 4.Change LDO 12VIN net name to 12VOUT |

**Accton Inc.**

**Making Partnership Work**

| | | |
|---|---|---|
| Model Name: **Wedge100 (ES7632BT-0415-168FB) PWR MODULE** | | Rev: R01 |
| Size: A | Page Name: HISTORY | Engineer: Mark_Huang |
| Date: Thursday, January 28, 2016 | Sheet 1 of 2 | |


CONN1
OUTA+_1
OUTA+_2
OUTA-_1
OUTA-_2
OUTB+_1
OUTB+_2
OUTB-_1
OUTB-_2
GND1
GND2
G1
G2
POWER
12VIN
HS1
@HEATSINK
Q3
Q4
Q1
Q6
Q7
NPN
R7
R8
100
C1
0.01uF
R9
2m ohm1%
R12
2m ohm1%
R16
2m ohm1%
R60
R59
R61
R62
R63
R64
1ohm
R21
10
C2
0.01uF
LTC4151_VIN
SENSE+
SENSE-
12VOUT
R25
20.5K
R26
20.5K
R27
220K
R28
68K
R32
10K
C4
0.1uF
C3
@1uF
R34
20.5K
C5
1000pF
GND_RPU
VCC
HGATE
SOURCE
DGATE
SENSE
OUT
SHDN
UV
OV
E-PAD
GND
FLT
ENOUT
TMR
FB
LTC4364HDE-2#TRPBF
D2
D1
1N4148W
@1N4148W
R33
200K
R35
20K
R36
0
R37
0
C6
1nF
PWR_INPUT_OK
PWR_OUTPUT_OK
Connect to main board
CONN2
P10A
P10B
P10C
P10D
P10E
P10F
P9A
P9B
P9C
P9D
P9E
P9F
P8A
P8B
P8C
P8D
P8E
P8F
P7A
P7B
P7C
P7D
P7E
P7F
P6A
P6B
P6C
P6D
P6E
P6F
P5A
P5B
P5C
P5D
P5E
P5F
P4A
P4B
P4C
P4D
P4E
P4F
P3A
P3B
P3C
P3D
P3E
P3F
P2A
P2B
P2C
P2D
P2E
P2F
P1A
P1B
P1C
P1D
P1E
P1F
3V3
R47
10K
R30
10K
for main board I2C
PWR_I2C_SDA
PWR_I2C_SCL
PWR_INPUT_OK
PWR_OUTPUT_OK
PWR_I2C_A0
PWR_I2C_A1
GND_RPU
R2 @1K
R3 @1K
R4 @1K
R5 @1K
R6 @1K
R10 @1K
R11 @1K
R12 @1K
R13 @1K
R15 @1K
R17 @1K
R18 @1K
R19 @1K
R22 @1K
R23 @1K
R24 @1K
R28 @1K
R31 0_Present
A1
B1
C1
D1
E1
A2
B2
C2
D2
E2
A3
B3
C3
D3
E3
A4
B4
C4
D4
E4
A5
B5
C5
D5
E5
Indication for input power ok and output power ok
HW Setting :UV:10V OV:14.6V
Output Clamping :13.6V
OCP Setting :74.6A
when power board remove form main board,Q7 turn on. and LTC4364 shutdown
U3
2Kb
A0
A1
A2
GND
VCC
WP
SCL
SDA
EEPROM_A2
EEPROM_WP
R41 @4.7K
R48 @4.7K
R51 4.7K
R49 @4.7K
R50 @4.7K
R45 @4.7K
R40 @4.7K
R42 4.7K
R43 4.7K
R44 4.7K
C7 0.1uF
3V3
I2C address 0x55
U2
AX1201SA
VCC
EN
FB
GND
OUT1
OUT2
OUT3
OUT4
R38 4.7K
C9 22uF
C10 0.1uF
R39 8.06K
C8 100pF
C11 22uF
C12 0.1uF
R46 4.7K
U4
LTC4151CMS#PBF
Vin
ADR1
ADR0
ADIN
SENSE+
SENSE-
SHDN
SDA
SCL
R54 @4.7K
R55 @4.7K
R52 4.7K
R53 4.7K
R58 @4.7K
R56 @4.7K
R57 @4.7K
I2C address 0x6f
H6 SCREW_8_1
H5 SCREW_8_1
H4 SCREW_8_1
H3 SCREW_8_1
H2 SCREW_8_1
H1 SCREW_8_1
R65
R66
R67
R68
R69
R70
Accton Inc.
Making Partnership Work
Model Name
Wedge100 (ES7632BT-0415-168FB) PWR MODULE
Rev
R01
Size
C
Page Name
Hot Swap
Engineer
Mark_Huang
Date:
Thursday, January 28, 2016
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